

FIG. 1

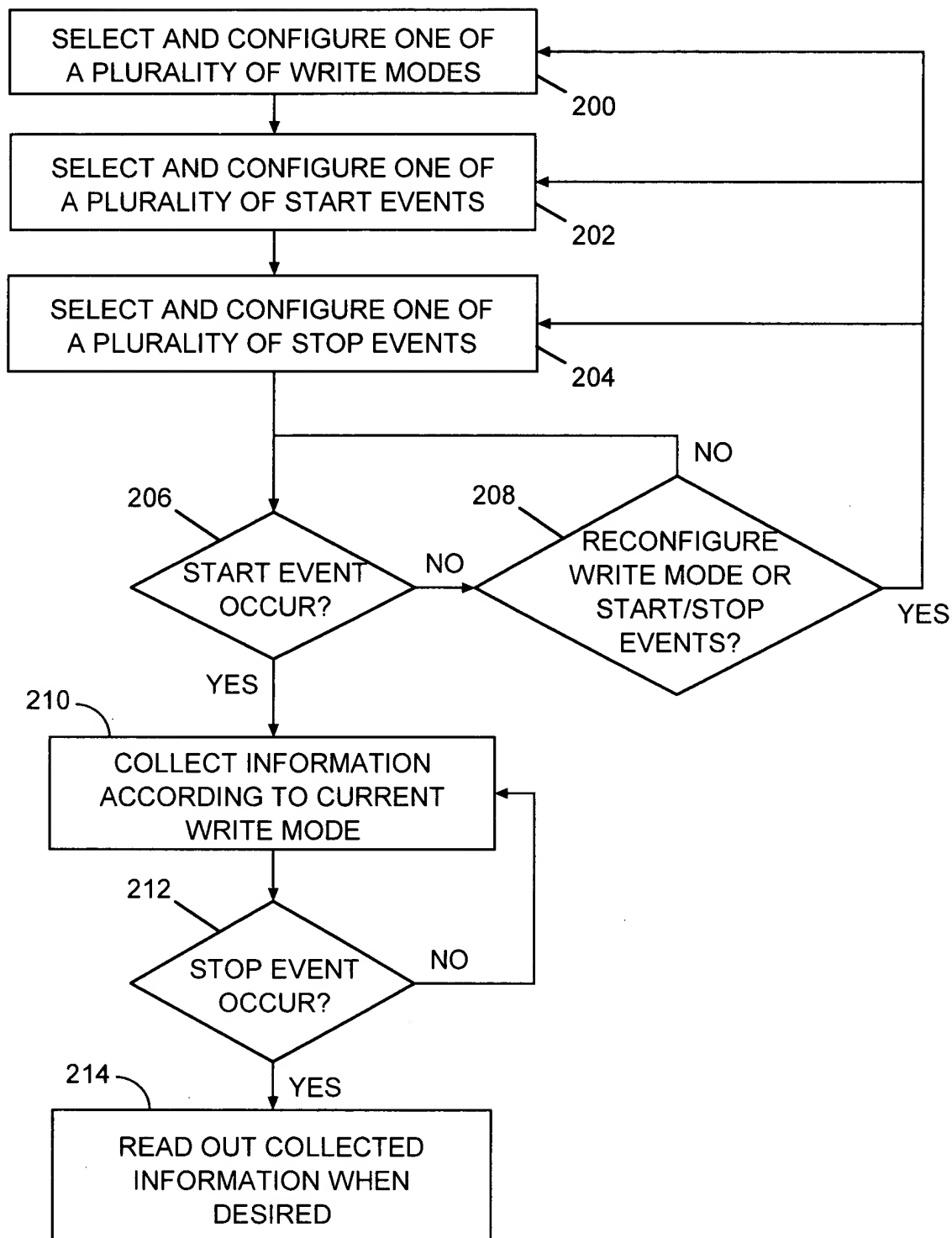


FIG. 2

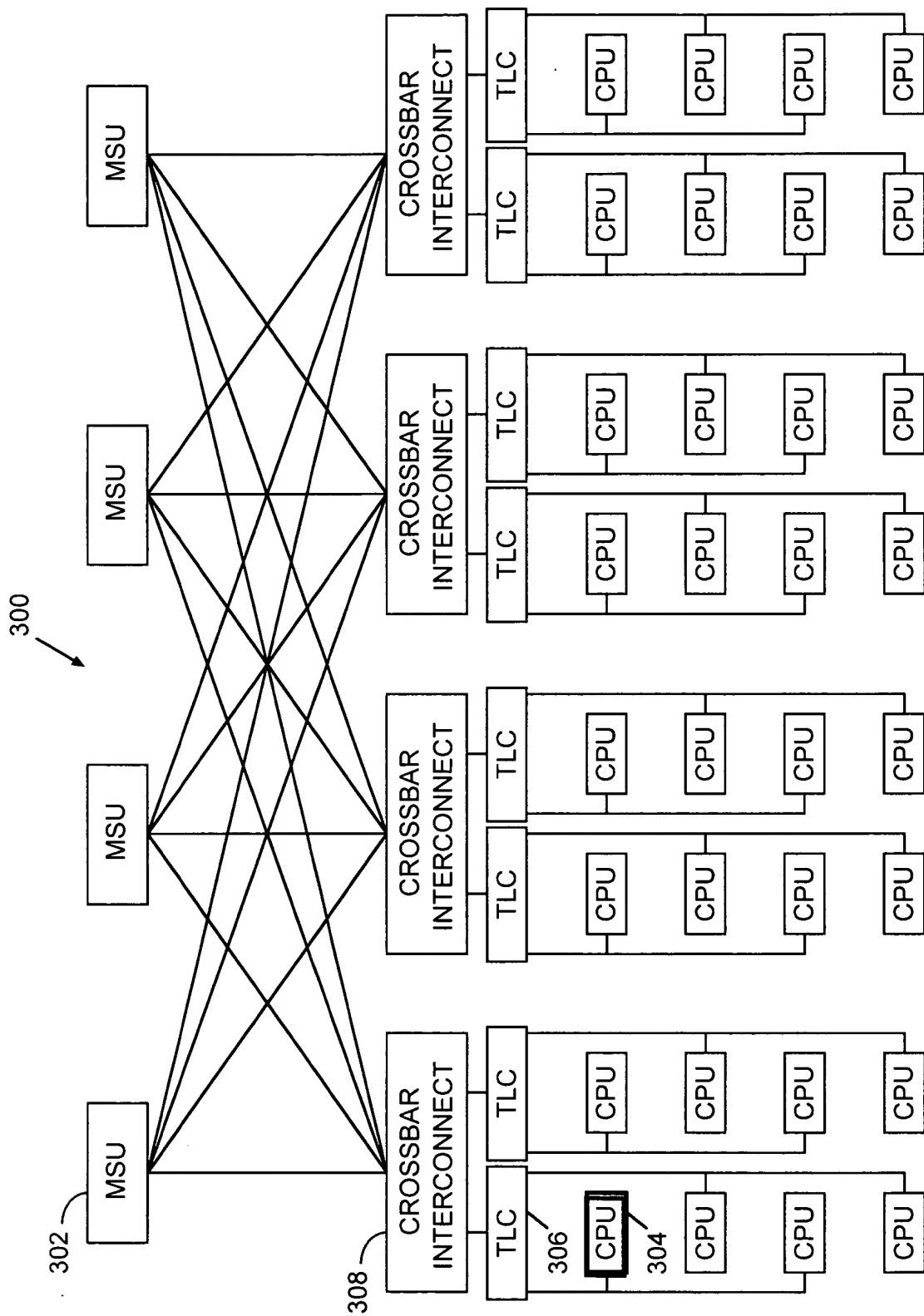


FIG. 3

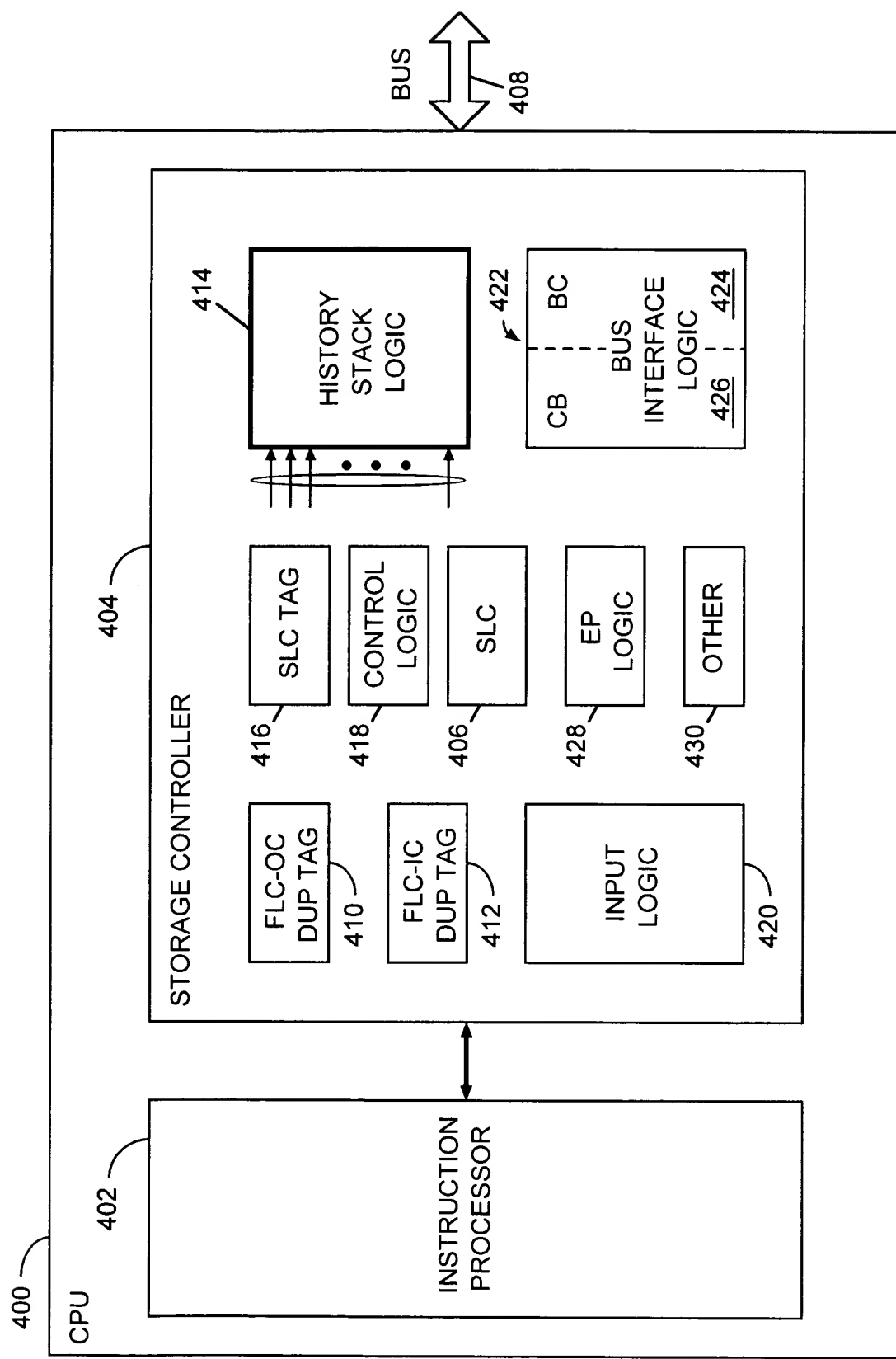


FIG. 4

500

502	WRITE MODE	504	REGISTER BITS
506	NORMAL	1	X X
508	CYCLE	0	1 1
510	FUNCTION/ADDRESS	0	0 0
512	DUPLICATE TAG	0	0 1
514	NORMAL OR DUPLICATE TAG	0	1 0

FIG. 5

600

602	START FEATURE	604	REGISTER BITS
606	ALWAYS ON	0	0
608	START ON F/A COMPARE	0	1
610	START ON IP SIGNAL	1	0
612	UNDEFINED	1	1

FIG. 6

700

702	STOP FEATURE	704	REGISTER BITS
706	NO STOP	0	0 0 0
708	STOP ON F/A COMPARE	0	0 1
710	STOP ON IP SIGNAL	0	1 0
712	STOP BEFORE OVERWRITE	0	1 1
714	STOP ON EP DETECTED CRITICAL ERROR	1	0 0
716	STOP ON EP DETECTED PASSIVE ERROR	1	0 1
718	STOP ON NO REQUEST FROM IP	1	1 0
720	STOP ON NO REQUEST FROM BUS	1	1 1

FIG. 7

